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<u>L5</u>	L4 and ((324/\$)!.CCLS.)	1	<u>L5</u>
<u>L4</u>	L2 and random ADJ number	11	<u>L4</u>
<u>L3</u>	L2 and random with number	26	<u>L3</u>
 L2	L1 and multiplexer and memory	71	<u>L2</u>
<u>L1</u>	integrated ADJ circuit with test\$3 and (test or mask) with vector and (deterministic or pseudo with random)	166	<u>L1</u>

END OF SEARCH HISTORY